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[54] **STORAGE UNIT SUBSYSTEM**

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[*] Notice: This patent is subject to a terminal disclaimer.

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[58] **Field of Search** 395/182.03, 182.04; 371/51.1, 53, 49.1, 40.11, 40.12, 40.15; 364/550

714/6.

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[57] **ABSTRACT**

When receiving a write request from a processor, a control unit checks the condition of existence (or the presence/absence) in a cache for information necessary for generation of an updated value of a parity record, receives write data and reports the completion of the write request to the processor. In asynchronism with the write request from the processor, the control unit performs a load process for that information among the information necessary for generation of the updated value of the parity record which may be prepared in asynchronism with the write request from the processor and a write after process for the updated value of the parity record.

5 Claims, 82 Drawing Sheets